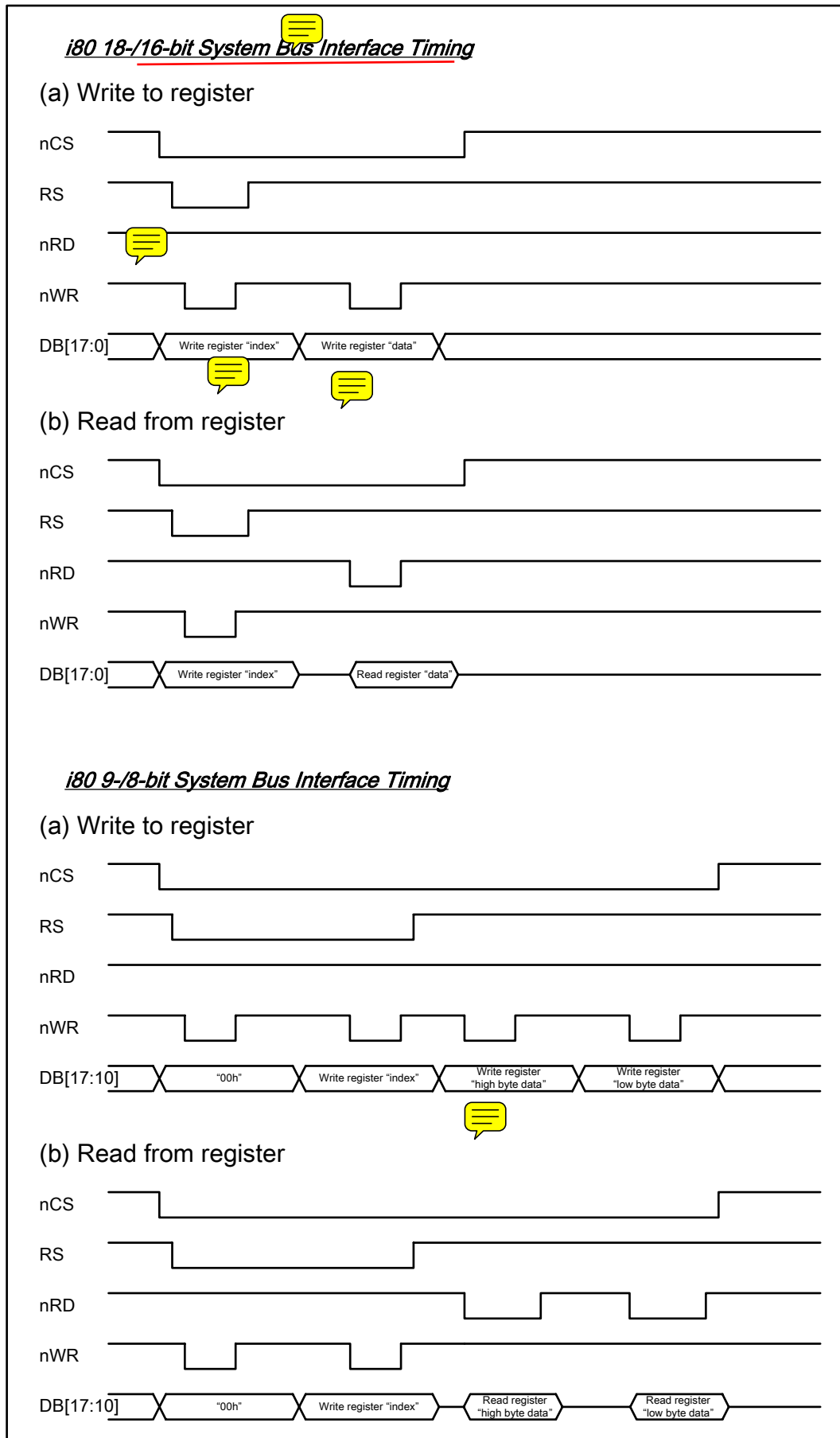




**Figure 24 Register Read/Write Timing of i80 System Interface**